Assessment and subject description

Óbuda University				Department of Microelectronics and						
Kandó Kálmán Faculty of Electrical Engineering				Technology						
Subject name and code: Analogue and Digital Technics KEXDTBABN							E Credits: 5			
Full-time, Spring Semester										
Course: Technical	Course: Technical Management									
Responsible: H	alázs Kovács, Ph.D. Teaching Márk Horváth									
Prerequisites: Electrotechnics KEXET1ABNE										
Contact hours	Lecture: 2	Lecture: 2 Class discussion: 2 Lab hours: 0 Tutorial:								
per week:										
Assessment and	Exam									
evaluation:										
	Subject description									
<i>Aims</i> : The subject's aim is to understand the basic properties and applications of basic semiconductor										
devices and circuits such as diodes, transistors and operational amplifiers.										
This course will give an overview of the basic concepts and applications of digital technics. In the										
course of lectures	, classroom-tu	torials ar	nd laborato	ry (exercises the future	technical m	anagement			
should acquire sol	id knowledge	and suffic	cient profic	ieno	cy in the functioning	g, operation,	design and			
applications of dig	ital systems.					-	U U			
Topics to be cover	ed: P-N juncti	on, diodes	. Bipolar tr	ansi	istors. Field-effect tr	ansistors. Fu	ndamentals			
of digital technics	. Logic (Bool	ean) algel	bra, logic o	per	ations and function	s. Combinati	onal logic,			
analysis and synth	hesis and imp	lementati	on of logi	c ci	rcuits. Binary arith	metics, algor	rithms and			
circuits. Code syst	ems, code con	version. C	Combination	nal (circuit functional bui	lding blocks	, properties			
and applications.										
		Topics				Week	Lessons			
Semiconductors.										
Intrinsic and doped semiconductors, n and p type crystal structures. Majority										
and minority charge carriers. Conductivity in semiconductors, drift and 1. 2+2										
diffusion currrent. p-n junction, space charge region, diffusion potential.										
Behavior of p-n junction due to external bias.										
Application of sem	Application of semiconductor diodes.									
The semiconductor diode. Thermal dependence and capacity of p-n junction. 2. 2.						2+2				
Concept of operati	Concept of operating point, static and dynamic resistance.									
Bipolar transistor.										
Structure, properties, characteristics and function of bipolar transistors. 3.							2+2			
Setting of operatin	Setting of operating point, thermal dependence.									
Amplification with	mplification with bipolar transistor.									
Physical process o	Physical process of amplification. CE, CC, CB basic circuits. Parameters of						2+2			
amplifiers.	plifiers.									
MOS-FET.										
Structure and ope	ration of MOS	S-FETs. E	Inhanceme	nt a	nd depletion MOS-	5.	2+2			
FET. Characteristi	cs. CMOS circ	uits.								
J-FET.					C	-				
Structure and oper	ation of J-FEI	. Charact	eristics. Set	ting	g of operating point;	6.	2+2			
thermal dependance	ce. Basic circui	ts.				_				
Test1						7.	2+2			
Fundamental concepts of digital technics and of logic networks. S					networks. Specific	8.	2+2			
characteristics of d	haracteristics of digital technics. Digital (binary) representation.									
Introduction to and applications of logic algebra. Description of logic										
connection: textua	I, algebraic for	m, truth ta	able, logic (liag	ram. Boolean	9.	2+2			
algebra: axioms an	a: axioms and theorems. Fundamental logic operations.									

Logic functions, fundamental concepts. Two-variable logic functions. Fully and incompletely specified logic functions. Canonic forms of logic functions. Disjunctive (sum-of-products, SOP), conjunctive (product-of-sum, POS) canonic forms, minterms and maxterms.	10.	2+2
Manipulation and transformation of logic functions. Graphic representation: Veitch diagrams and Karnaugh maps). The concept and methods of logic function minimization.	11.	2+2
Number systems, fundamentals. Binary numbers. Arithmetic operations in the binary number systems.	12.	2+2
Codes and encoding, fundamental concepts. Numeric and alphanumeric codes. Pure binary codes (direct, 1s complement, 2s complement codes. Arithmetic operations in 1s and 2s complement codes. Tetrad codes, BCD codes. Arithmetic operations in tetrad and BCD codes	13.	2+2
Test2	14.	2+2

Assessment and evaluation

Requirements of the signature:

To attend the lectures and class discussion is obligatory. Above that it is required to pass two tests.

Type of exam: Written and oral

Evaluation of the exam:

To pass the exam to reach at least 50% is required.

Suggested material

Comment:

The lecture's materials are the basics of the learning process. They could be found on the concerned web sites of the university.