## Assessment and subject description

Óbuda University									
Kandó Kálmán Faculty of Electrical Engineering De						Department of Microelectronics and Technology			
Subject name and code: Electronic Technology KEXET5ABNE						Credits: 4			
Full-time, Spring	Semeste	r							
Course: Electrica	l Enginee	ring							
Responsible:	Csikósné	Dr Pap	Andrea	Teaching staff:		Gröller György, Nádas József, Dr.			
						Ürmös Anta	Antal, Meszlényi György		
Prerequisites:		KMEVR	12AND						
Contact hours	Lecture	e: 2	Class discu	ussion: 0	Lab ho	urs: 2	Tutorial:	0	
per week:									
Assessment and	exam								
evaluation:									
			Subj	ect descript	tion				
Aims: Review ma	terials ar	nd proce	sses used i	n electroni	c industi	y. Constructi	ons of micro	pelectronic	
parts and dev	ices and	their	manufact	uring met	hods. I	Basic techno	ologies of	electronic	
interconnections	. Microel	ectronic	s is one of	the main f	ield of h	itech. To und	derstand the	advanced	
products is neces	sary to k	now the	ir technolo	gical backgr	round.				
Laboratory aims:	To devel	op labor	atorial skill	s in the fiel	d of PCB	technology.			
Topics to be cove	ered:								
			Topics				Week	Lessons	
Introduction to t	he techno	ology an	d electroni	ic industry	Discrete	parts,			
substrates, integrated circuits, modules and devices						1	2		
Manufacturing of Printed Wiring Boards: patterning; steps of lithography,									
screen printing, etching, electroless and galvanic plating.							2	2	
Single and double side PCB; main steps of production. Multilayer PCB-s,								2	
coo-laminated and sequential methods.							3	2	
High Density Interconnections (HDI); new requirements, new processes.									
Control methods.							4	2	
Design for Manufacturing (DfM).							4	2	
<i>Encapsulation;</i> types and footprint of the electronic parts									
Manufacturing a	of the elec	ctronic n	nodules; Su	Irface Mou	nted Tec	hnology			
(SMT)						F	2		
Soldering basics. Solder paste printing, shooting of devices, reflow 5								2	
soldering.									
SMT II: wave sold	SMT II: wave soldering, inspection methods, rework. ESD protection. 6						2		
test	test						7	2	
Hybrid Integrate	d Circuits	; (HIC)					0		
Thin Film HIC: vacuum deposition methods.						8			
Thick Film HIC: screen printing methods						0	2		
Thin and thick pa	ssive circ	uits, trin	nming metl	hods			9	2	
Thick Film HIC: so	reen prir	nting me	thods						
Thin and thick passive circuits, trimming methods						10	2		
Multichip Modul	<i>es:</i> types,	manufa	cturing me	thods					
Introduction to t	he semic	onducto	r technolo <u>c</u>	<b>y:</b> Materia	ls (silico	n and			
compounds sem	iconducto	ors)	_				10	2	
Main processes of	Main processes of IC technology: lithography, doping, oxidizing, etching,						10	2	
epitaxy and vacu	um depo	sition m	ethods						

Main processes of IC technology: lithography, doping, oxidizing, etching,		
epitaxy and vacuum deposition methods	11	2
Micro Electro-Mechanical Systems (MEMS)	12	2
Printed electronics: materials and technology	13	2
Consultation	14	2
Laboratory Topics		
ntroduction, working and safety rules	1	3
Manufacturing: Double side, through hole plated PCB. drilling, making hole conductive	2	3
Photolithography, galvanic plating	3	3
Solder mask preparation and patterning	4	3
Assembly processes, soldering TH and SM devices	5	3
Design: Circuit diagram I, borders, finding parts, choosing encapsulation. Block processes,	6	3
Circuit diagram II Drawing a schematic: finding parts, choosing package footprint, wiring, block operations. Board module, practise	7	3
Routing, placing components. Auto routing, manual routing	8	3
Design Rule Check (DRC), practising. Demo	9	3
Assessment and evaluation		
Requirements of the signature: The test result better than 40%		
Type of exam: Written exam		
Evaluation of the exam: 0 – 49 % 1 50 – 59 % 2 60 – 69 % 3 70 – 84 % 4 85 – 100% 5		
Suggested material		
Gröller György: Electronic technology (presentations and handouts) in Mood	le	
Recommended: Happy Holden: The HDI Handbook <u>http://www.hdihandbook</u>		bad.php
Comment:		